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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of: Noel B. Dequina, et al.

Filed: April 21, 2004

Serial No.: 10/828,610

For: HIGH VOLTAGE GATE DRIVER

USING A LOW VOLTAGE MULTI-

LEVEL CURRENT PULSE

**TRANSLATOR** 

Atty. Docket: INSL:0101

Examiner: Unassigned

Group Art Unit: Unassigned

## CERTIFICATE OF MAILING

Pursuant to 37 C.F.R. Sec. 1.8, I hereby certify that this paper is being deposited with the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 14, 2004.

2313-13-0, Oil May 14, 2004.

Helen Q. Morgan

## FORMAL DRAWING TRANSMITTAL LETTER

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed herewith please find:

\_\_\_\_\_\_ sheets of formal drawings. Please substitute these formal drawings for the informal drawings originally filed.

The enclosed drawings each bear the Applicant's Name, Serial Number, Attorney Name and Telephone Number and Docket Number on the reverse side. Examiner's approval of the entry of these drawings is respectfully requested. No new matter has been added.

If there are any questions regarding this submission, please direct them to the undersigned Attorney of Record for Applicant.

Respectfully submitted,

Date: May 14, 2004

Gary R. Stanford

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